

Table of Contents	Description
<p>1) Overview</p> <ul style="list-style-type: none"> • Table of Contents • List of Figures and Tables • Introduction • Device Summary 	<p>The Process Flow Analysis from Chipworks is a valuable addition to a standard Structural Analysis Report (SAR) on memory and image sensor devices. Can also be ordered as custom analysis on other device types.</p>
<p>2) Estimated Process Sequence</p> <ul style="list-style-type: none"> • Evidence based discussion of process mask sequence in sections numbered 0), 1), 2), etc. beginning at the substrate. 	<p>Using images from the SAR for evidence, plus other images and analysis as needed, the sequence is described point form with discussion of process steps for each mask step.</p>
<p>3) Summary Mask Count</p> <ul style="list-style-type: none"> • Summary table showing the process mask sequence. 	<p>A numbered summary of the Mask Count</p> <ul style="list-style-type: none"> • Including minimum critical dimension for each mask.

Please note, due to the nature of reverse engineering, Chipworks report content may vary. Reports published prior to the latest revision date (August 2007) may contain different content than stated above.

